

# HPC Asia 2004

## CALL FOR PAPERS

The 7th International Conference  
on High Performance  
Computing and Grid in  
Asia Pacific Region

<http://www.hpcc.jp/HPCAsia2004>  
Omiya Sonic City, Tokyo, Japan  
July 20-22, 2004

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## SCOPE

HPCAsia is an international conference series held every 18 months on an Asia-Pacific regional site. It provides a forum for HPC researchers, developers, and users throughout the world to exchange ideas, case studies, and research results related to all issues of high performance computing. The seventh conference, HPCAsia 2004, will be held in Omiya Sonic City, Saitama in Tokyo area, Japan on July 20-22, 2004.

Authors are invited to submit manuscripts of original unpublished research work in all areas of high performance computing, including the development of experimental and commercial systems. Relevant topics include (but are not limited to) the following:

- System Architecture and Models for HPC
- System Software for HPC
- Algorithms and Applications for HPC
- Grid Computing

## TECHNICAL PAPER SUBMISSION

Authors are invited to submit papers of no more than 8 pages of double column text using single spaced 10 point size type on 8.5x11 inches papers, as per IEEE 8.5x11 manuscript guidelines (see <http://www.computer.org/cspress/instruct.htm>).

Authors should submit a PostScript (level 2) file that will print on a PostScript printer using 8.5 x 11 inch size (letter size) paper. Paper submission instruction will be placed on the conference official website.

The results presented in the paper must be original. One author of each accepted paper will be expected to present the paper at the conference. It is expected that the proceedings will be published by the IEEE Computer Society Press.

## IMPORTANT DATES

Submission deadline	January 31, 2004
Acceptance notification	March 31, 2004
Camera ready deadline	May 7, 2004

## WORKSHOPS

Several workshops are planned as mini-symposiums of the conference. Emerging topics on the workshops will include:

- ApGrid
- CFD and CM for the New Century
- PC Cluster Advanced Technologies and Experiences in Computer Centers
- Simulation Results on the Earth Simulator
- Bio Grid
- Reconfigurable System for HPC
- National Research Grid Initiative of Japan (NAREGI)

The submission to the workshops will be announced on the conference website soon. If a workshop has more accepted papers than allowed by the workshop time slots, some papers will be recommended to be incorporated into the regular sessions.

## Sponsored by:

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- IEEE CS Japan Chapter

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